



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

~~ATTORNEY:~~ Patent Application Serial No. 10/026,078
 APPLICANT: AUBURY, MATTHEW
 TITLE: SYSTEM, METHOD, AND ARTICLE OF MANUFACTURE
 FOR ESTIMATING A POTENTIAL PERFORMANCE OF A
 CODESIGN FROM AN EXECUTABLE SPECIFICATION
 FILING DATE: 12/21/2001
 GROUP ART UNIT: 2123
 ATTORNEYDOCKET NO.: 44359/08327 EMB1P072

#2
A. Catto
3/29/04

INFORMATION DISCLOSURE STATEMENT

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P.O. Box 1450
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Technology Center 2100

Sir:

In accordance with 37 C.F.R. § 1.98, Applicant, through his undersigned attorney, hereby makes of record the prior art set forth on the attached forms PTO-SB/08A and PTO-SB/08B. One copy of each item of prior art noted on this form is enclosed for consideration by the Examiner.

Respectfully submitted,

Respectfully submitted,
C. Douglas McDonald

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Applicant: AUBURY, MATTHEW
Title: SYSTEM, METHOD, AND ARTICLE OF
MANUFACTURE FOR ESTIMATING A
POTENTIAL PERFORMANCE OF A CODESIGN
FROM AN EXECUTABLE SPECIFICATION

Serial No. 10/026,078
Filed: 12/21/2001

Examiner: SHARON, AYAL I
Attorney Docket No.: 44359/08327 EMB1P072

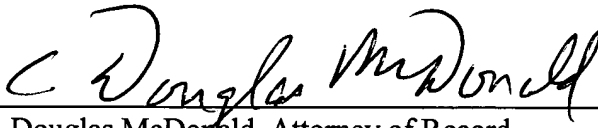
Group Art Unit: 2123

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C. Douglas McDonald, Attorney of Record

3/15/04
Date

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March 15, 2004

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Attention: Mr. Ayal I. Sharon, Patent Examiner

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Re: Patent Application Serial Number: 10/026,078
Our File Number: 44359/08327 EMB1P072

Dear Mr. Sharon:

We are enclosing an Information Disclosure Statement in connection with the above-referenced application. Included with this Information Disclosure Statement are the International Search Report issued by the European Patent Office and one copy of each of the documents cited, all of which were recently received from our associate handling the prosecution of that European Patent Office counterpart to the present application. If you have any questions regarding these materials, please contact me.

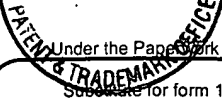
Very truly yours,

C. Douglas McDonald

CDM/hmu
Enclosures

cc: Mr. Stephen Dyde (w/encl.)

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Application Number	10/026,078
Filing Date	12/21/2001
First Named Inventor	Matthew Aubury
Art Unit	2123
Examiner Name	Ayal I. Sharon
Attorney Docket Number	44359/08327 EMB1P072

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Application Number	10/026,078
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First Named Inventor	Matthew Aubury
Art Unit	2123
Examiner Name	Ayal I. Sharon
Attorney Docket Number	44359/08327 EMB1P072

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FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶	
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)					
		WO 0038087 A	29-06-2000	AU 12-07-2000	1875200 A		
			GB 07-11-2001	2362005 A			
			WO 29-06-2000	0038087 A1			

**Examiner
Signature**

Date
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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PTO/SB/08B (08-03)

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Matthew Aubury

Art Unit

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Examiner Name

Ayal I. Sharon

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NON PATENT LITERATURE DOCUMENTS

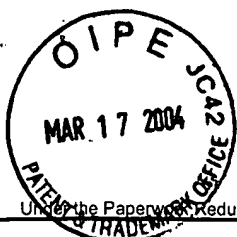
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		O'NILS M ET AL: "Design of D-AMPS channel decoder with codesign methodologies" BEC '96. THE 5TH BIENNIAL BALTIC ELECTRONICS CONFERENCE. PROCEEDINGS, BEC '96 5TH BIENNIAL	
		BALTIC ELECTRONICS CONFERENCE. PROCEEDINGS, TALLINN, ESTONIA, 7-11 OCT. 1996, pages 397-400, XP002243970 1996, Tallinn, Estonia, Tallinn Tech. Univ, Estonia ISBN: 9985-59-026-0	
		the whole document	
		YE W ET AL: "Fast timing analysis for hardware-software co-synthesis" COMPUTER DESIGN: VLSI IN COMPUTERS AND PROCESSORS, 1993. ICCD '93. PROCEEDINGS., 1993 IEEE INTERNATIONAL	
		CONFERENCE ON CAMBRIDGE, MA, USA 3-6 OCT. 1993, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, 3 October 1993 (1993-10-03), pages 452-457, XP010134577 ISBN: 0-8186-4230-0 the whole	
		document	
		HARDT W ET AL: "Speed-up estimation for HW/SW-systems" PROCEEDINGS. FOURTH INTERNATIONAL WORKSHOP ON HARDWARE/SOFTWARE CO-DESIGN (CODE/CASHE '96) (CAT. NO.96TB100020),	
		PROCEEDINGS OF 4TH INTERNATIONAL WORKSHOP ON HARDWARE/SOFTWARE CO-DESIGN.. CODES/CASHE '96, PITTSBURGH, PA, USA, 18-20 MARCH 1996, pages 36-43, XP002243932 1996, Los Alamitos,	
		CA, USA, IEEE Comput. Soc. Press, USA ISBN: 0-8186-7243-9 the whole	
		document	
Examiner Signature		Date Considered	

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		EDWARDS M D ET AL: "Software acceleration using programmable hardware devices" IEE PROCEEDINGS: COMPUTERS AND DIGITAL TECHNIQUES, IEE, GB, vol. 143, no. 1, 25 January 1996	
		(1996-01-25), pages 55-63, XP006006162 ISSN: 1350-2387 the whole document	

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